

Fig. 1

The diagram shows a common-emitter amplifier circuit. A dashed vertical line on the left represents the input signal path. Component 1 is a circle at the input. Component 2 is a BJT transistor. Component 3 is a coupling capacitor between the input and the base of the transistor. Component 4 is a biasing network consisting of two capacitors in series to ground. Component 5 is a load resistor connected between the collector of the transistor and ground. The output is taken from the collector and is labeled 'OUT'. The supply voltage is labeled 'Vcc'.

Fig. 2

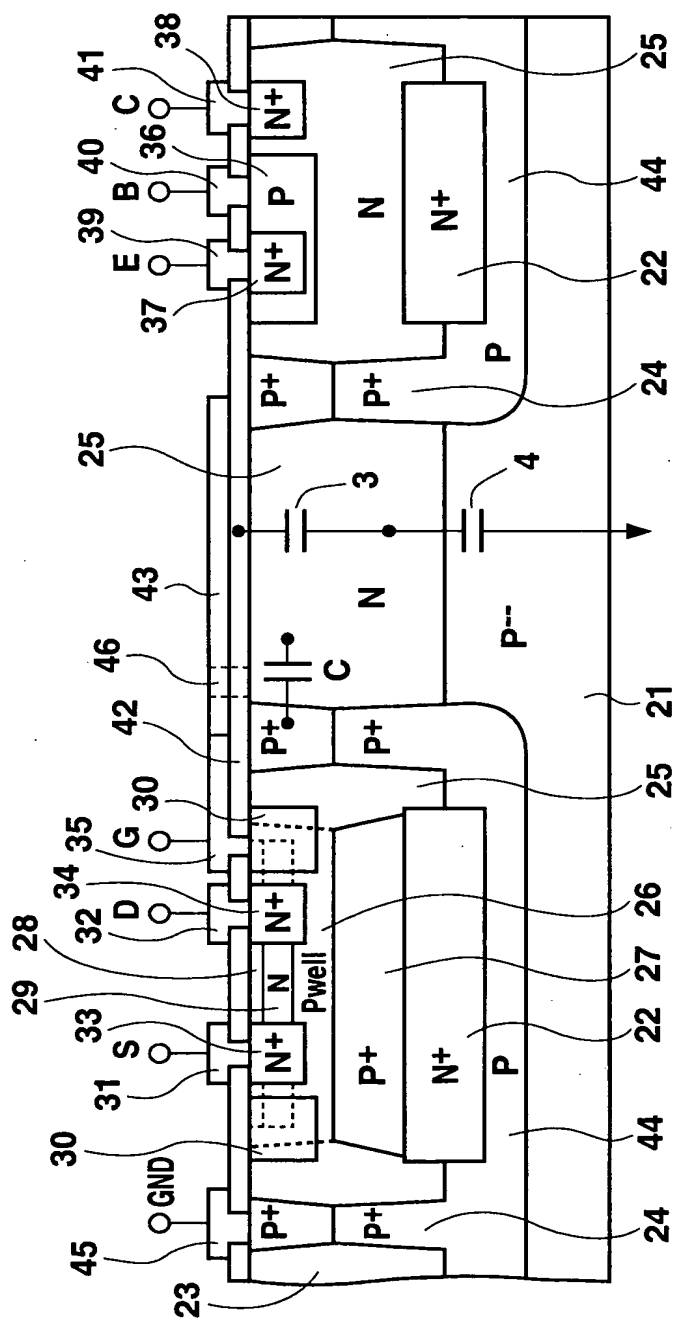


Fig. 3

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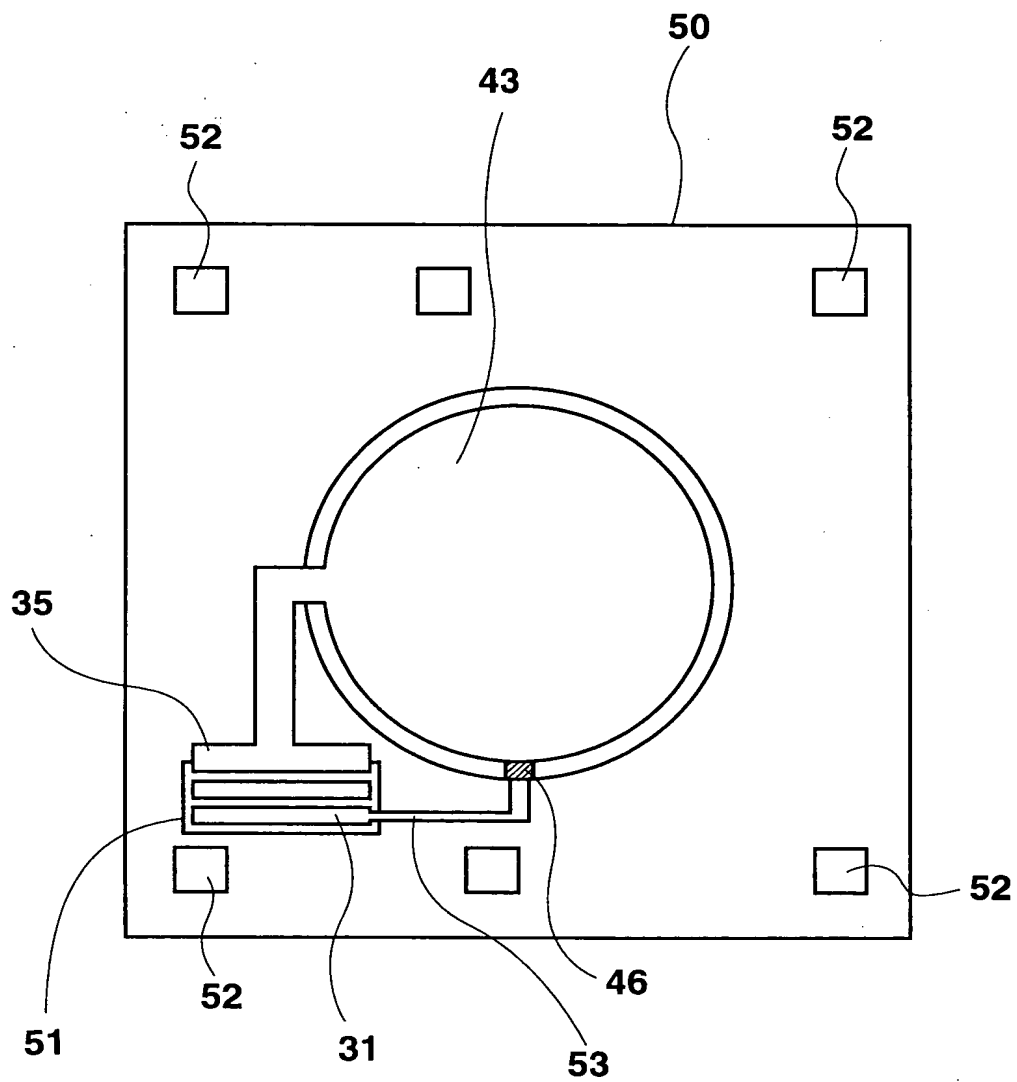


Fig. 4

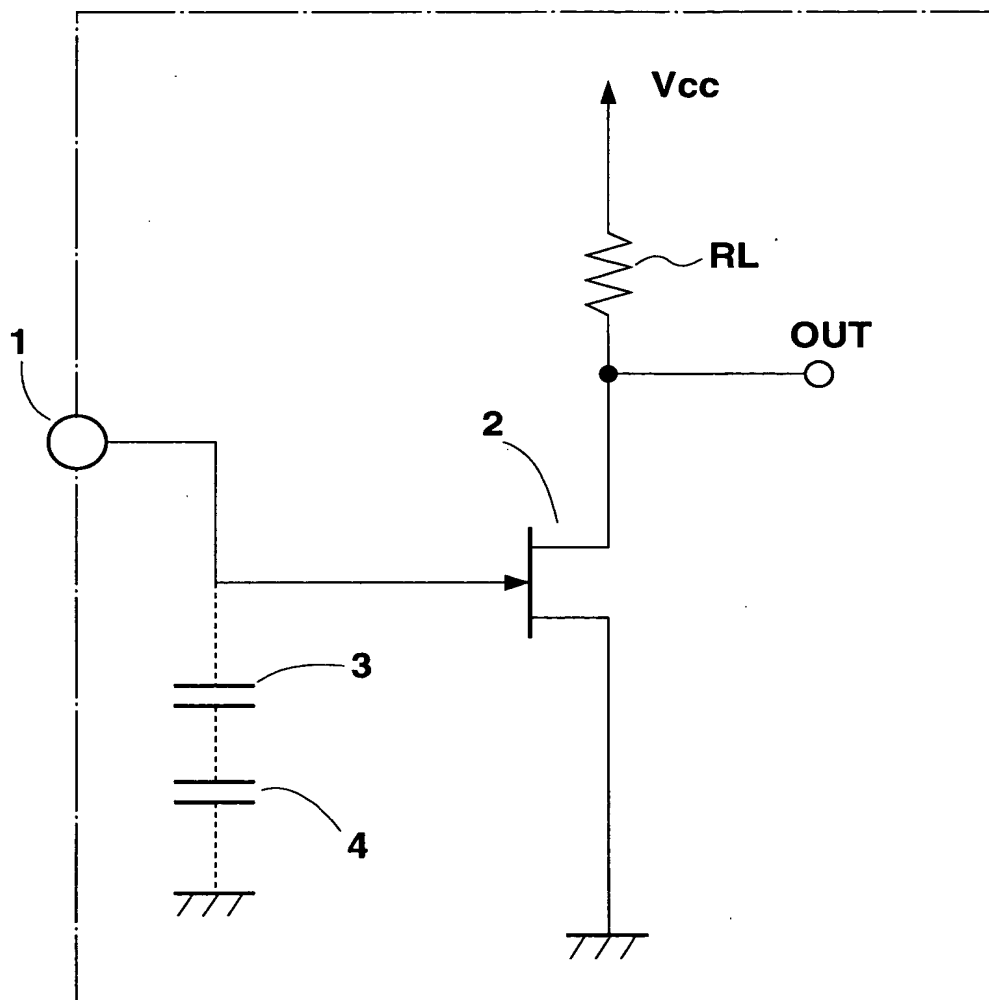


Fig. 6 PRIOR ART

